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PATENT

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*Vickie D. Wall*  
Vickie D. Wall

Applicant : Alexander G. MacInnis, et al. Confirmation No.6408  
Application No. : 10/763,087  
Filed : January 22, 2004  
Title : GRAPHICS DISPLAY SYSTEM WITH UNIFIED MEMORY ARCHITECTURE  
Grp./Div. : 2181  
Examiner : To be assigned  
Docket No. : 51718/JEJ/B600

**5th SUPPLEMENTAL  
INFORMATION DISCLOSURE STATEMENT  
37 CFR § 1.97(b)**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Post Office Box 7068  
Pasadena, CA 91109-7068  
July 19, 2005

Commissioner:

In compliance with the duty of disclosure under 37 CFR §§ 1.56, 1.97 and 1.98, and in accordance with the provisions in the Manual of Patent Examining Procedure §§ 609 and 707.05(b), enclosed is FORM PTO/SB/08A/B listing the references that are known to applicant. Copies of each of the references listed under "OTHER DOCUMENTS" are enclosed. This filing is timely because it is made during one of the periods described in 37 CFR § 1.97(b).

It is respectfully requested that the listed references be considered in the examination of this application and identified on the list of references cited on the patent issuing for this application. Applicant also requests that an initialed copy of FORM PTO/SB/08A/B be entered in the application file and returned to applicant with the next communication from the Office in accordance with MPEP § 609.

Respectfully submitted,  
CHRISTIE, PARKER & HALE, LLP

By *Jun-Young E. Jeon*  
Jun-Young E. Jeon  
Reg. No. 43,693  
626/795-9900

JEJ/vdw  
Enclosures: PTO/SB/08A/B, w/references (2)

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FORM PTO/SB/08A/B (10-01)  
Substitute for PTO-1449A/B

**INFORMATION DISCLOSURE**

**STATEMENT BY APPLICANT**

(use as many sheets as necessary)

Attorney Docket Number	51718/JEJ/B600
Application Number	10/763,087
Filing Date	January 22, 2004
Applicant(s)	Alexander G. MacInnis, et al.
Group Art Unit	2181
Examiner Name	To be assigned

**U.S. PATENT DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	DOCUMENT NUMBER Number - Kind Code <sup>2</sup> (If Known)	PUBLICATION DATE MM-DD-YYYY	NAME OF PATENTEE
		4,412,294	10-25-1983	Watts et al.
		5,625,764	04-29-1997	Tsujimoto et al.
		5,687,306	11-11-1997	Blank
		5,708,457	01-13-1998	Otake et al.
		5,877,817	03-02-1999	Moon
		5,909,559	06-01-1999	So
		6,081,297	06-27-2000	Lee
		6,085,273	07-04-2000	Ball et al.
		6,327,002 B1	12-04-2001	Rinaldi et al.
		6,466,581 B1	10-15-2002	Yee et al.
		6,470,100 B2	10-22-2002	Horiuchi
		6,853,385 B1	02-08-2005	MacInnis et al.

**FOREIGN PATENT DOCUMENTS**

EXAMINER INITIALS	Cite No. <sup>1</sup>	Foreign Patent Document Country Code <sup>3</sup> - Number <sup>4</sup> - Kind Code <sup>5</sup> (If Known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	T <sup>6</sup> (✓)

EXAMINER SIGNATURE		DATE CONSIDERED	
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. <sup>1</sup>Applicant's unique citation designation number (optional). <sup>2</sup>See Kinds Codes of USPTO Patent Documents at [www.pto.gov](http://www.pto.gov) or MPEP 901.4. <sup>3</sup>Enter Office that issued the document, by the two-letter code (WIPO standard ST.3). <sup>4</sup>For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. <sup>5</sup>Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST.16 if possible. <sup>6</sup>Applicant is to place a check mark here if English Language Translation is attached.

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<b>FORM PTO/SB/08A/B (10-01)</b> Substitute for PTO-1449A/B  <b>INFORMATION DISCLOSURE</b>  <b>STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)	<b>Attorney Docket Number</b>	<b>51718/JEJ/B600</b>
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OTHER DOCUMENTS		
EXAMINER INITIALS	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article, title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.
		Hudson, Scott, Desktop PC-IC Content and Integration Trends, 3/1/1999; Electronic News, <a href="http://www.reed-electronics.com/electronicnews/article/CA69496.html">http://www.reed-electronics.com/electronicnews/article/CA69496.html</a> .
		Brown, Peter, Basic Notebooks Infrastructure In Place - Trident introduces Monterey chip; Neomagic re-engineering products, Sept 28, 1998, Electronic News, <a href="http://www.findarticles.com/p/articles/mi_m0EKF/is_n2238_v44/ai_21172858">http://www.findarticles.com/p/articles/mi_m0EKF/is_n2238_v44/ai_21172858</a>

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